

Electronic Acknowledgement Receipt

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International Application Number:	
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Title of Invention:	NONPLANAR SEMICONDUCTOR DEVICE WITH PARTIALLY OR FULLY WRAPPED AROUND GATE ELECTRODE AND METHODS OF FABRICATION
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	Foreign Reference	1_JP06224440.pdf	67534 05edf9a607128f41a13045c2c4c30a64954d082	no	1
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Information:					
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3	Foreign Reference	3_JP09162301.pdf	79409 e7b226678137c3bcb285024506e02745a04c2	no	1
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4	Foreign Reference	4_TW200402872.pdf	111225 53b7b165463074c4cc9cf8005c562ed1198cb1ed	no	2
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5	Foreign Reference	5_TW200405408.pdf	68410 f77d5b4168c2bc7c5f71313cb10a61ba45cd883	no	1
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6	Foreign Reference	6_TW516232.pdf	102020 80655904d734b073ad0a06130e9024c6deca9d0	no	2
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7	Foreign Reference	7_TW548799.pdf	73580 012c5620bc2ab1362ca96a9114c7994044a9c	no	1
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10	NPL Documents	10_42P20379_050207_OA.pdf	1963648 700756c4c131060abc5c2714bda8f729252a7f16e0	no	47
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11	NPL Documents	11_42P20379111307_FOA.pdf	1262340 021eadd1e140c43c2d622d4607273ac7506778e5	no	15
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12	Foreign Reference	12_42P21096TW_062008_OA.pdf	696114 8c91db1859c79216d883c72a3a16104e7dd3835	no	8
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15		42P15685_IDS_102708.PDF	126647 d8d475c563298d27812713055efab3ba7c2d8	yes	4
	Multipart Description/PDF files in .zip description				
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	Information Disclosure Statement Letter		1	2	
	Information Disclosure Statement (IDS) Filed (SB/08)		3	4	
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16	Fee Worksheet (PTO-06)	fee-info.pdf	30816 <small>d:25927848866c5d8f792e4b027a5190e7c266e</small>	no	2
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